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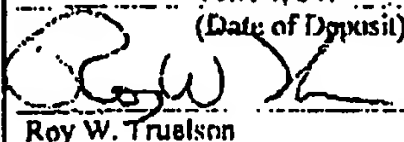
PATENT - AMENDMENT AFTER FINAL  
Response Under 37 C.F.R. 1.116 -- Expedited  
Procedure -- Examining Group 2187

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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AUG 31 2004

In re application of: Donald L. Freerksen, et al. : Date: June 1, 2004  
Group Art Unit: 2187 : IBM Corporation  
Examiner: B. Truong : Intellectual Property Law  
Serial No.: ~~09/862,486~~ 09/866,981 : Dept. 917, Bldg. 006-1  
Filed: May 23, 2001 : 3605 Highway 52 North  
Title: METHOD FOR INCREASING : Rochester, MN 55901  
EFFICIENCY IN A MULTI-PROCESSOR  
SYSTEM AND MULTI-PROCESSOR  
SYSTEM WITH INCREASED EFFICIENCY

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

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|--|----------------|
| I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on |                |
| June 1, 2004<br>(Date of Deposit)  |                |
| <br>Roy W. Truelson   | 6/1/04<br>Date |

Sir:

ENTERED!  
BT 09/27/04

AMENDMENT AFTER FINAL (37 C.F.R. 1.116)

This paper is being filed in response to the final office action dated March 29, 2004.  
Please amend the above identified application as follows.

Docket No.: ROC9-1997-0187-US2  
Serial No.: 09/866,981